


[illegible][illegible]

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
	Eles, P., et al., "Scheduling of Conditional Process Graphs for the Synthesis of Embedded Systems" Proc. 1 st ACM/IEEE Design and Test in Europe Conf., Paris France, pp. 132-138, February 1998.
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	Hoang, P., "Scheduling of DSP Programs onto Multiprocessors for Maximum Throughput", IEEE Trans. on Signal Processing, Vol. 41, No. 6, June 1993.
	Hong, I, et al., "On-Line Scheduling of Hard Real-Time Tasks on Variable Voltage", IEEE International Conf. On Comp. Aided Design, Santa Clara, CA, pp. 653-656, November 1998.
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	Tuck, B., "Raise your Sights to the System Level – Design Report 1997 Paris Forum", Computer Design, pp. 53-74, June 1997.

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